

FIG. 1

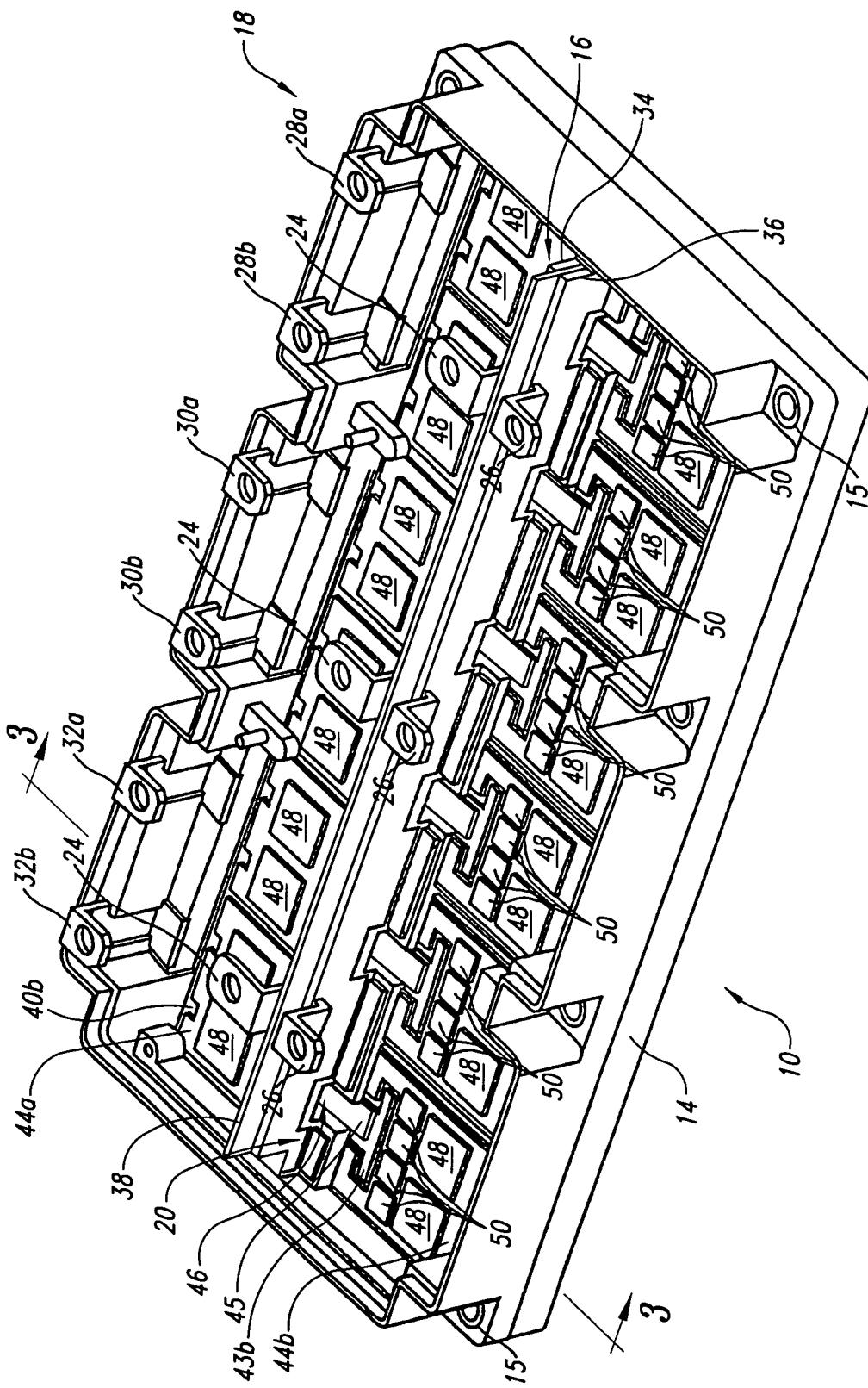


FIG. 2A

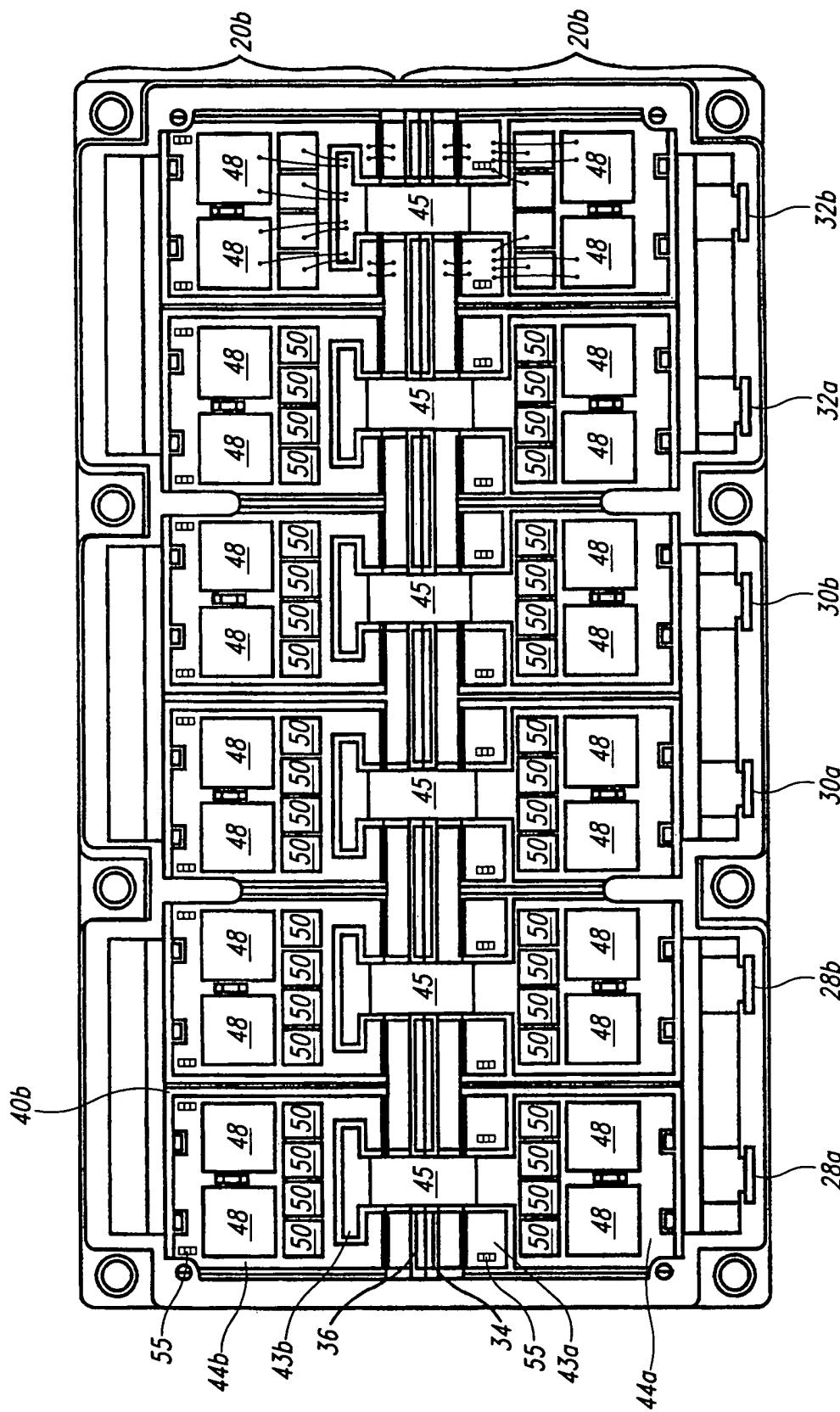


FIG. 2B

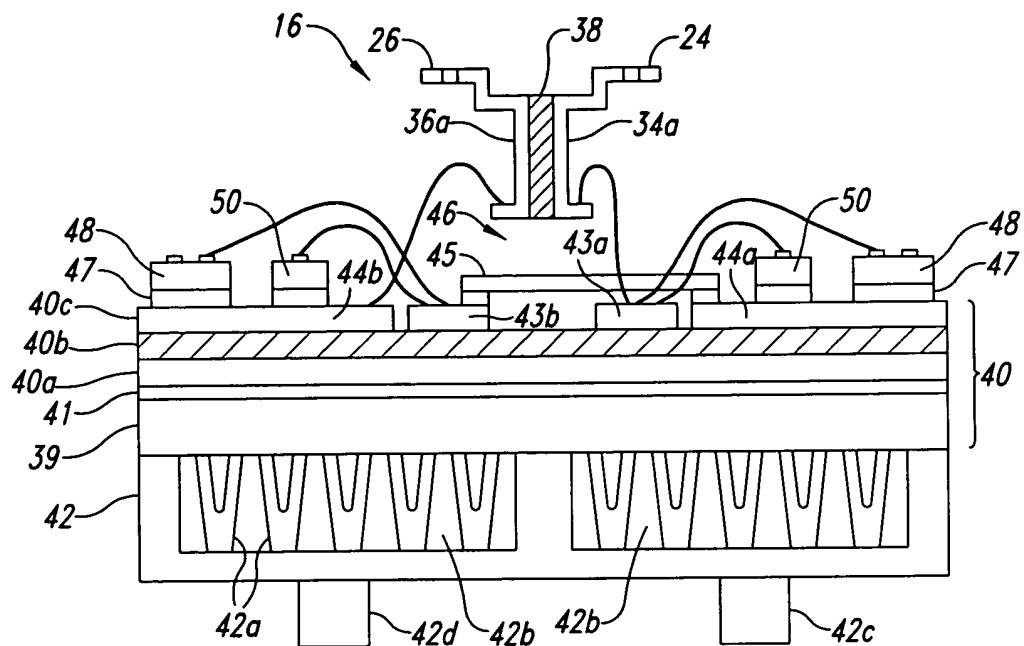


FIG. 3

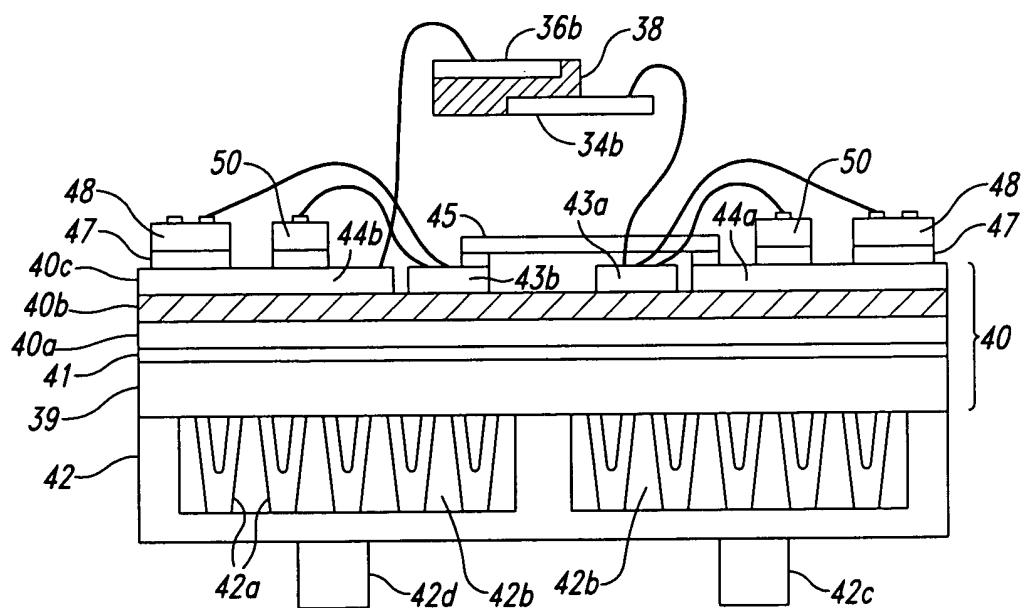


FIG. 4

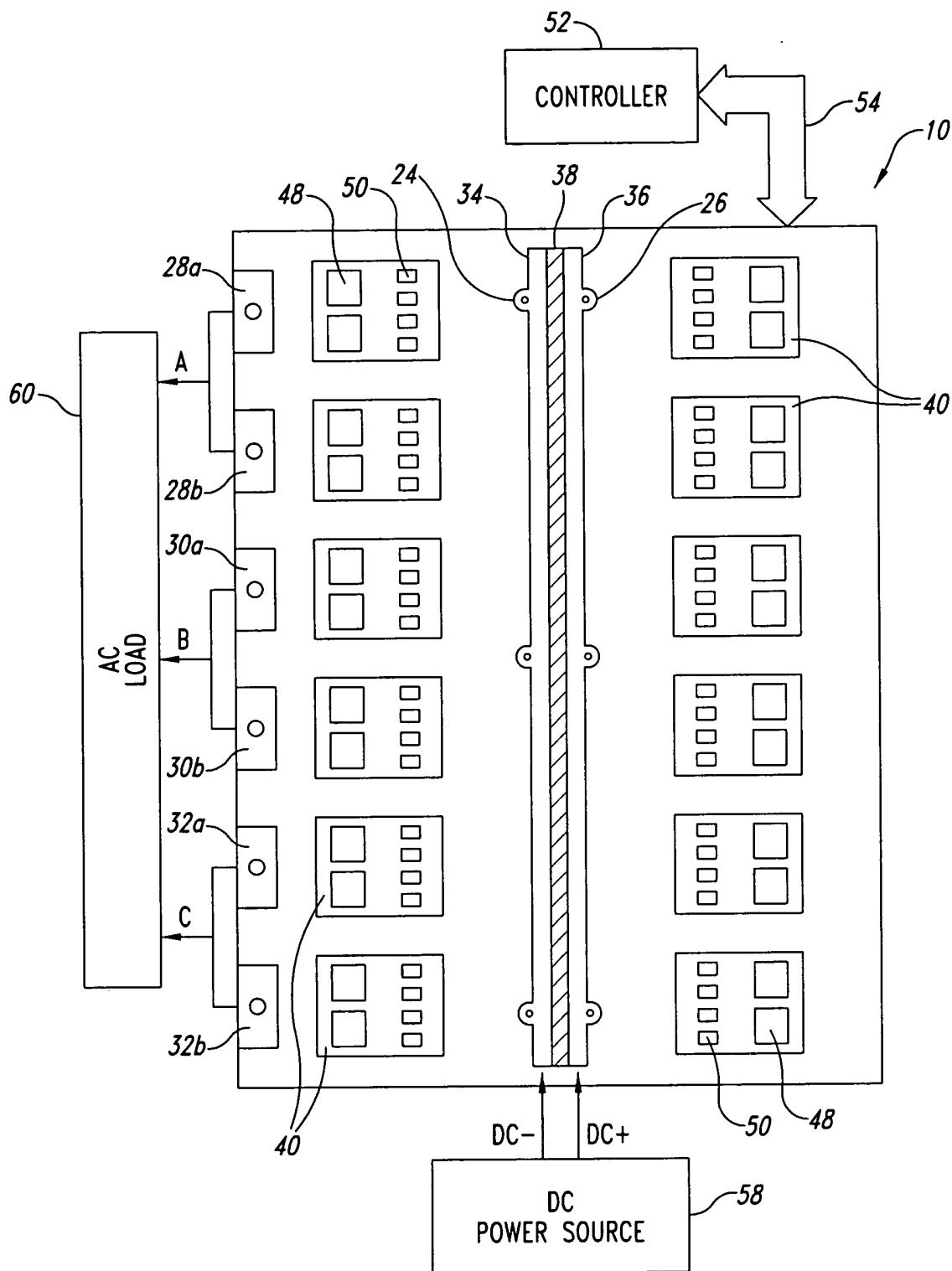


FIG. 5

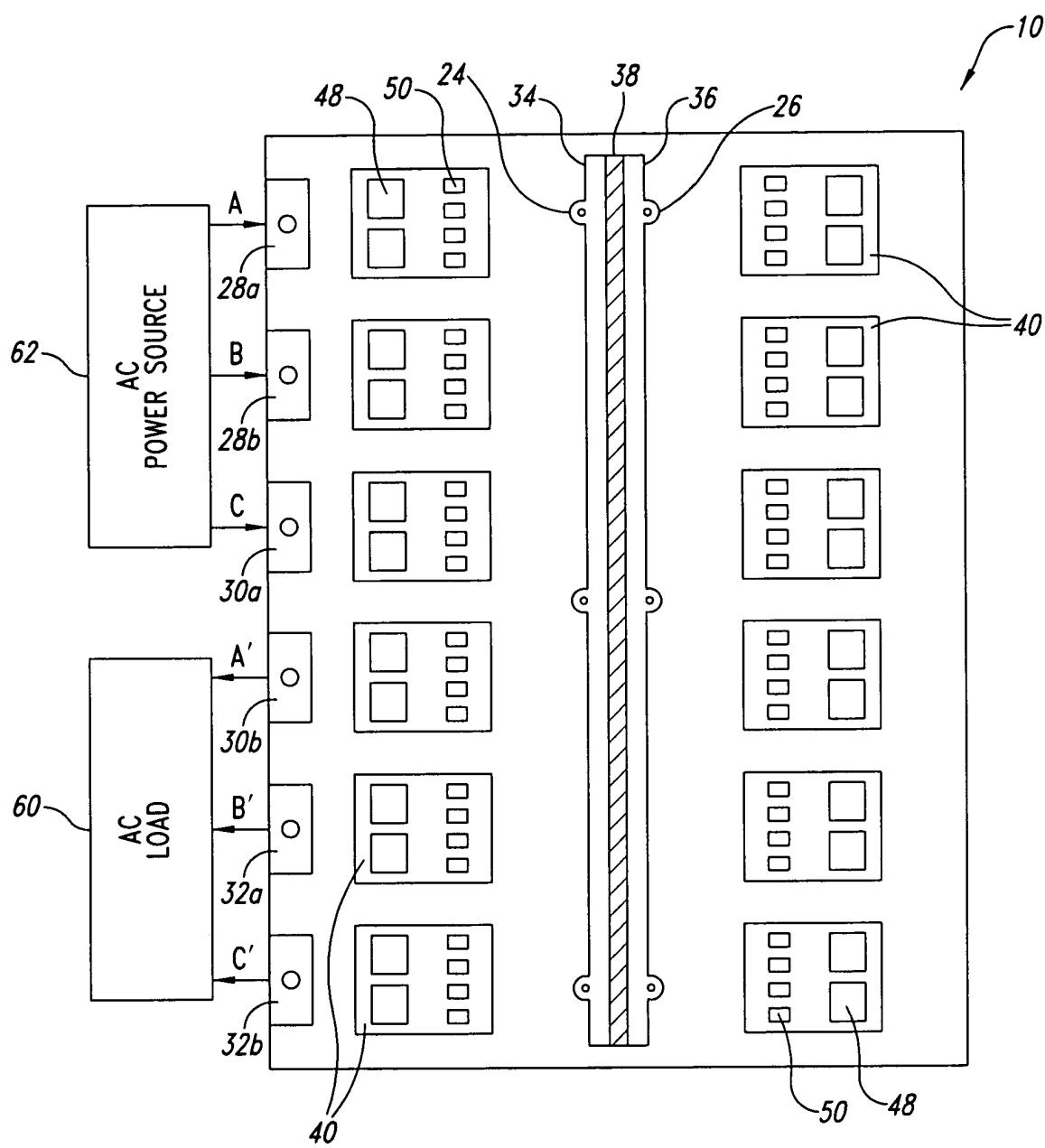


FIG. 6

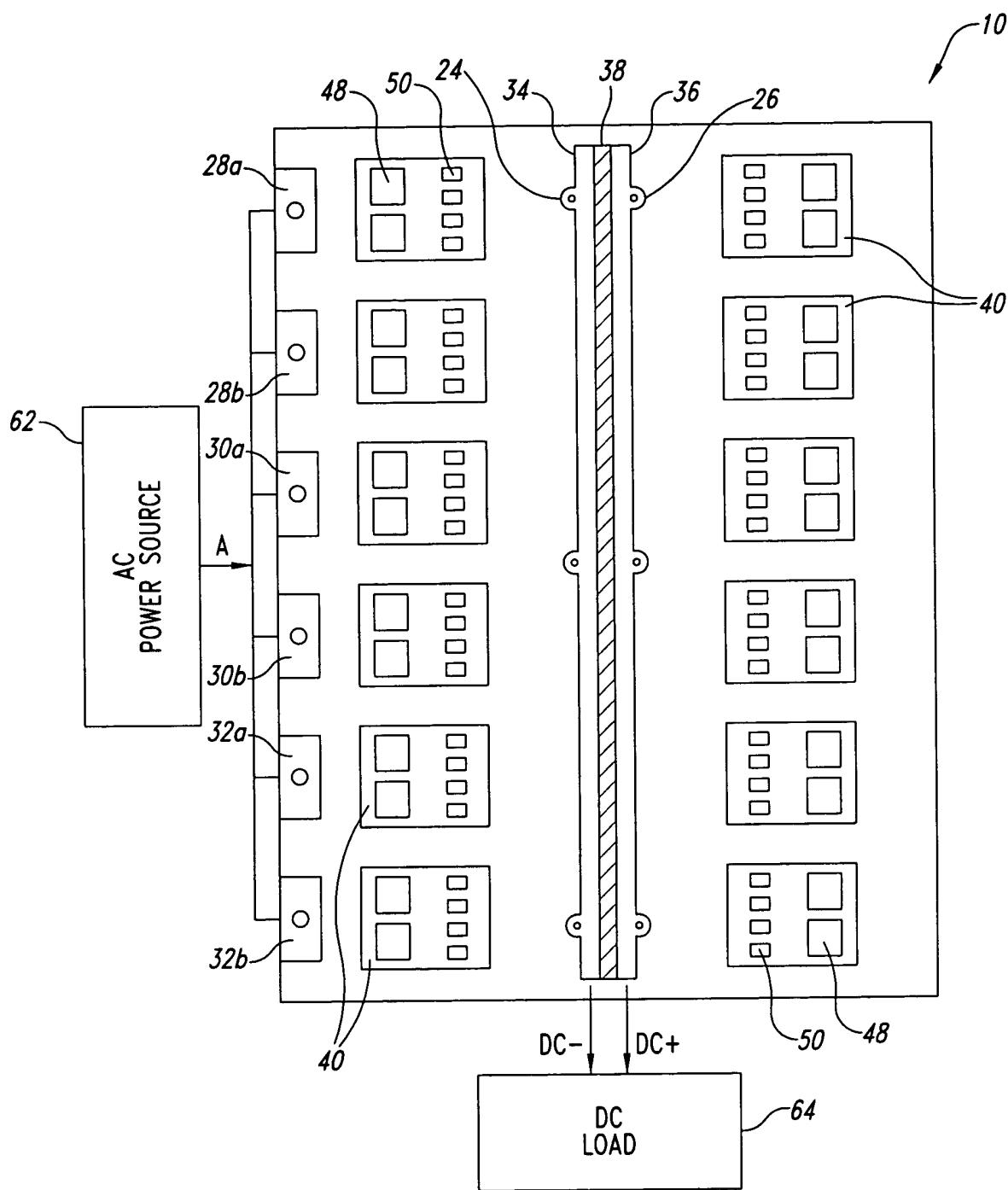


FIG. 7

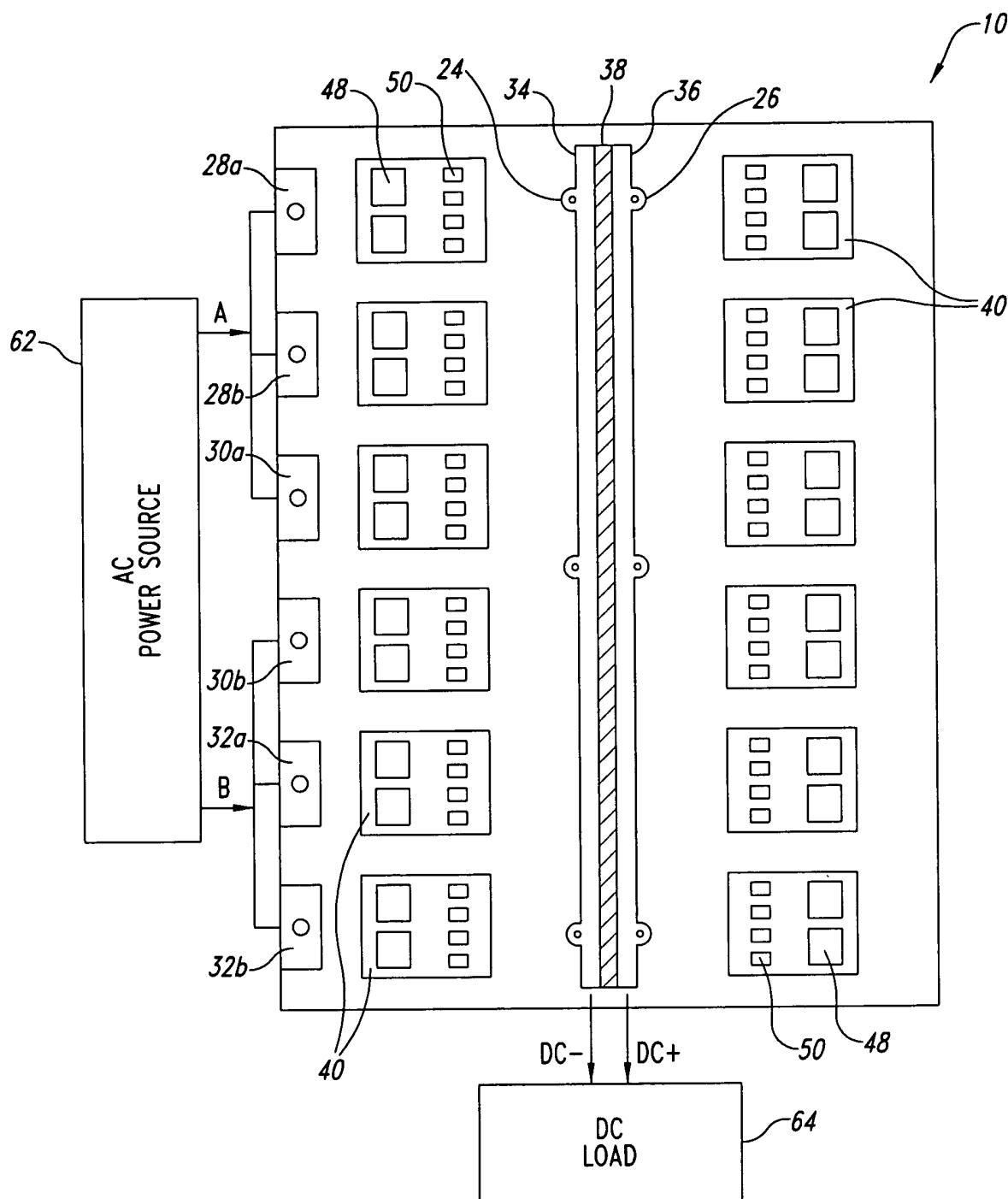


FIG. 8

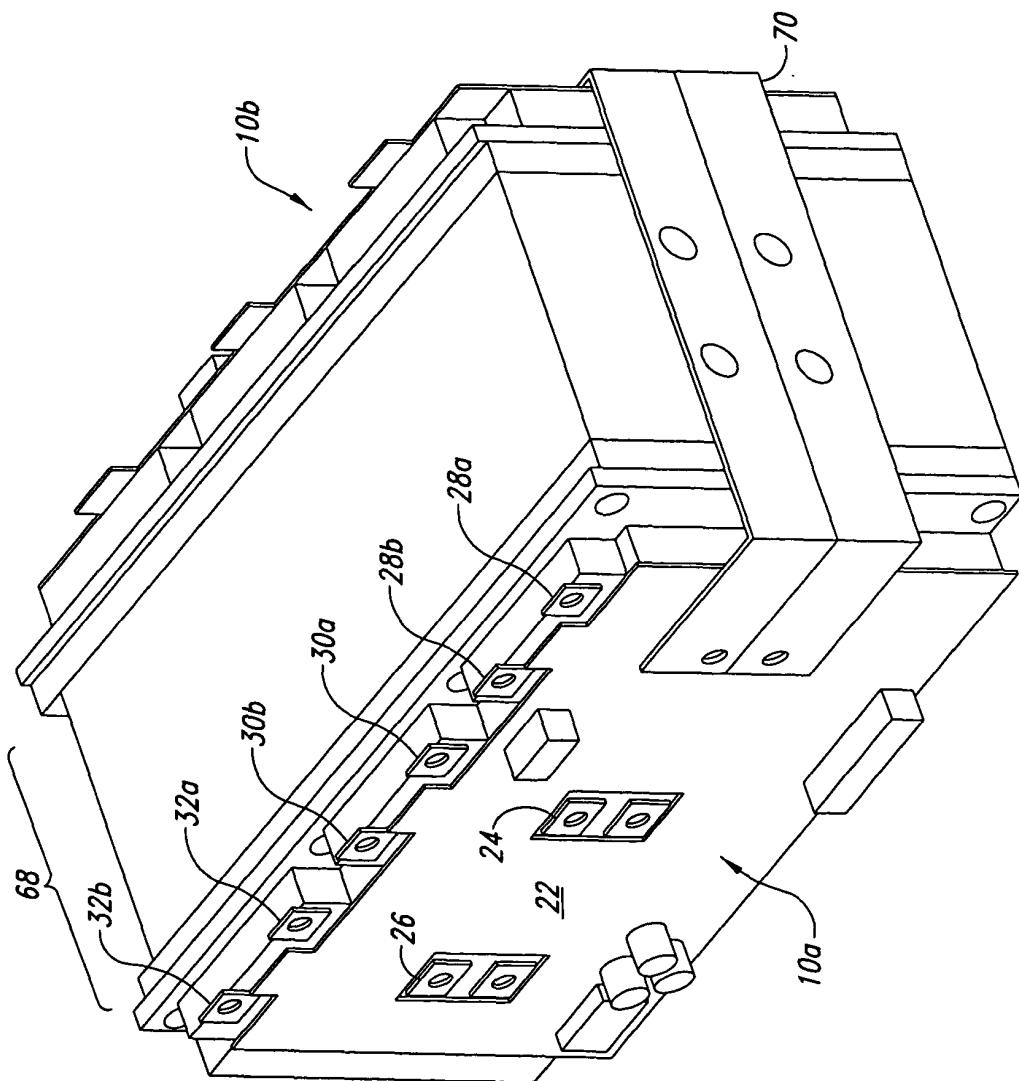


FIG. 9

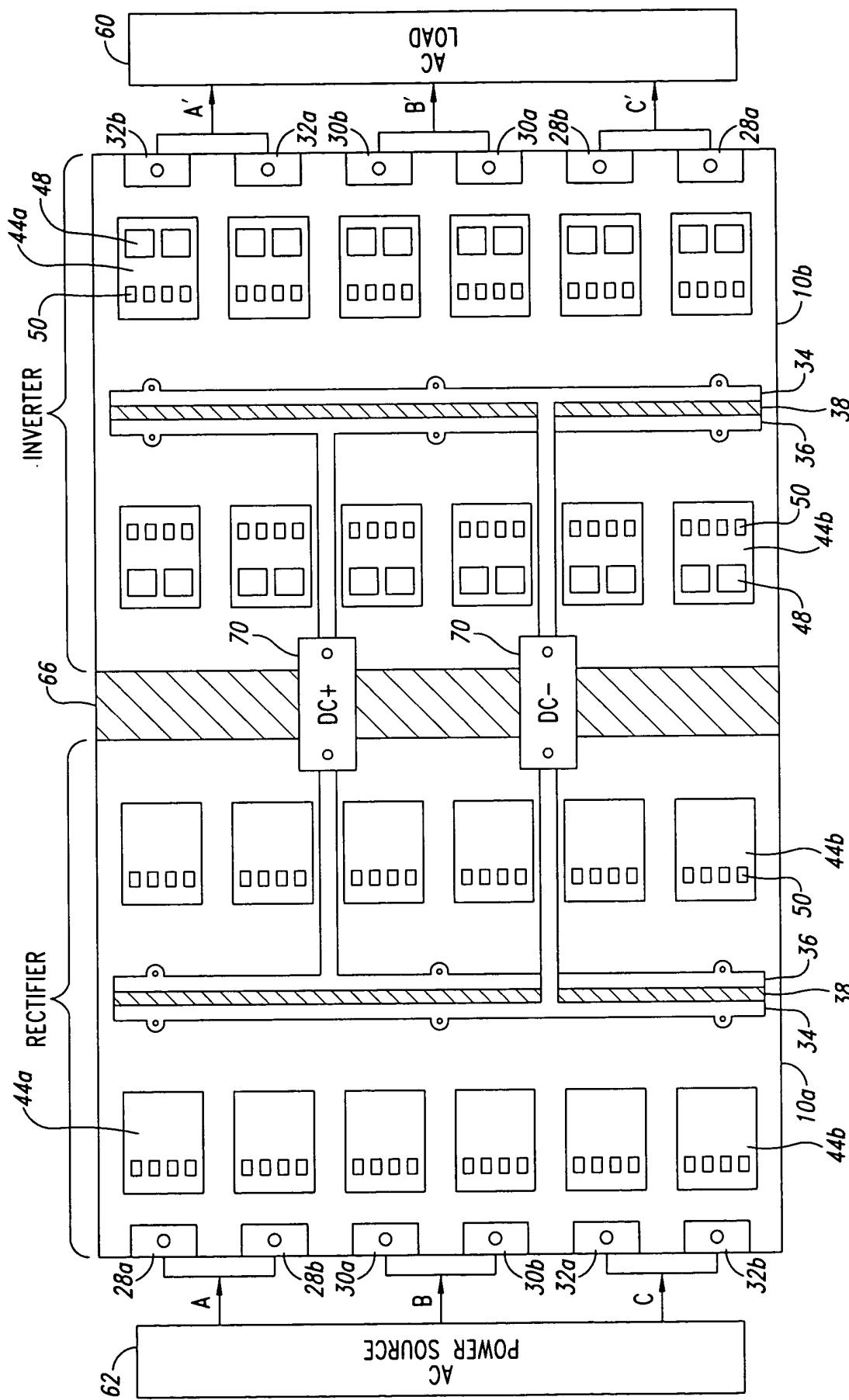


FIG. 10

Title: DUAL POWER MODULE POWER SYSTEM ARCHITECTURE

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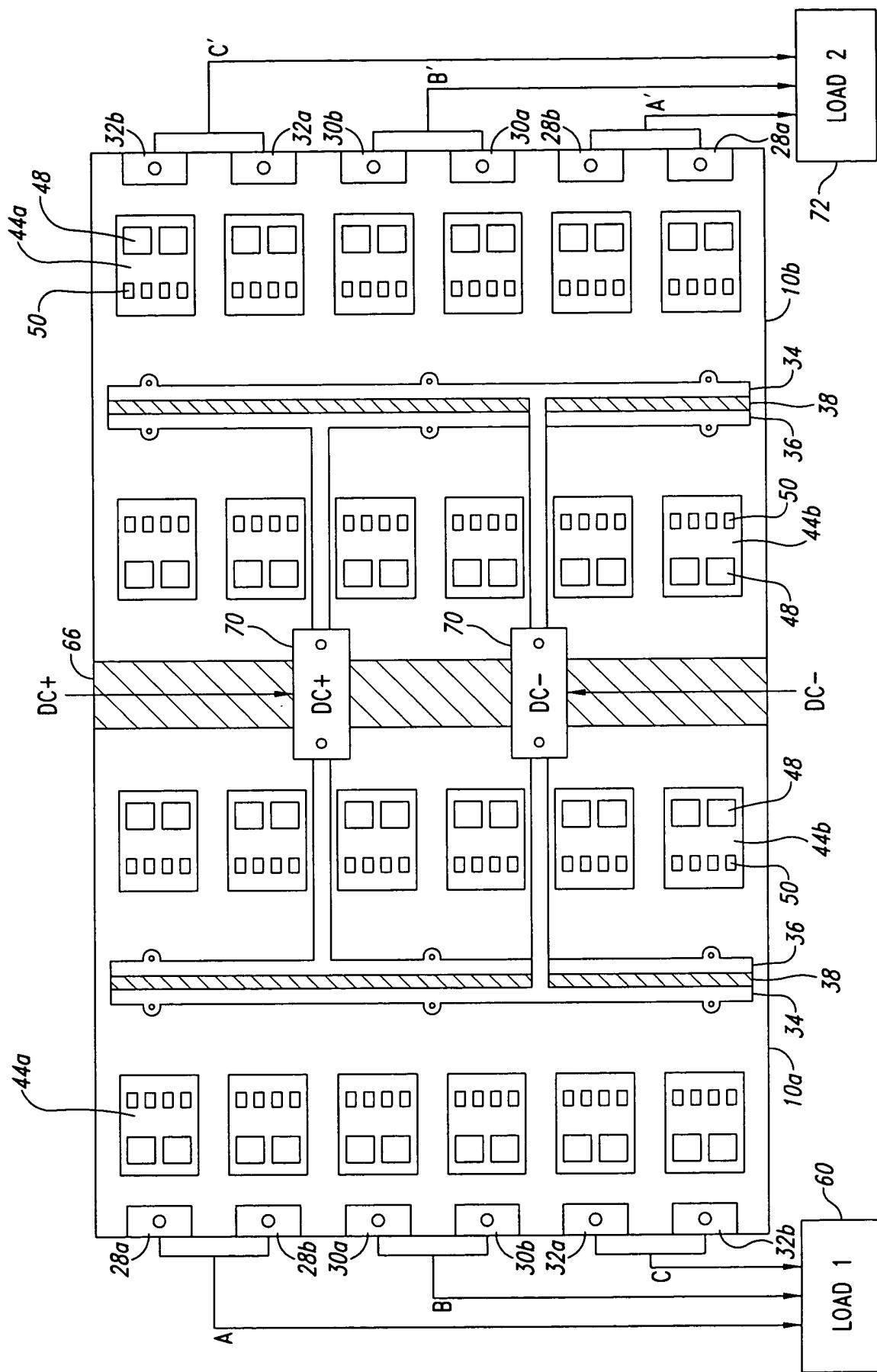


FIG. 11

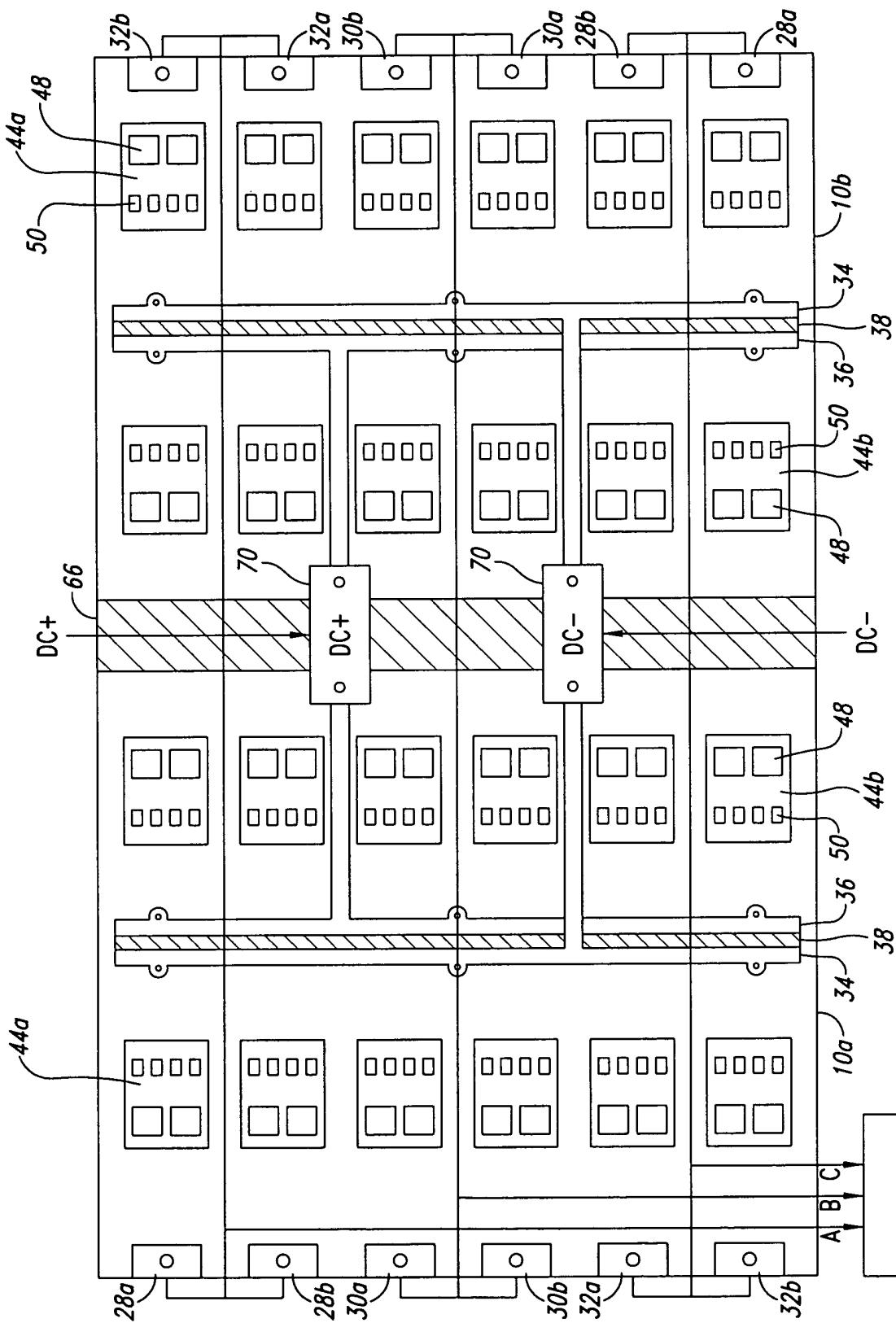


FIG. 12

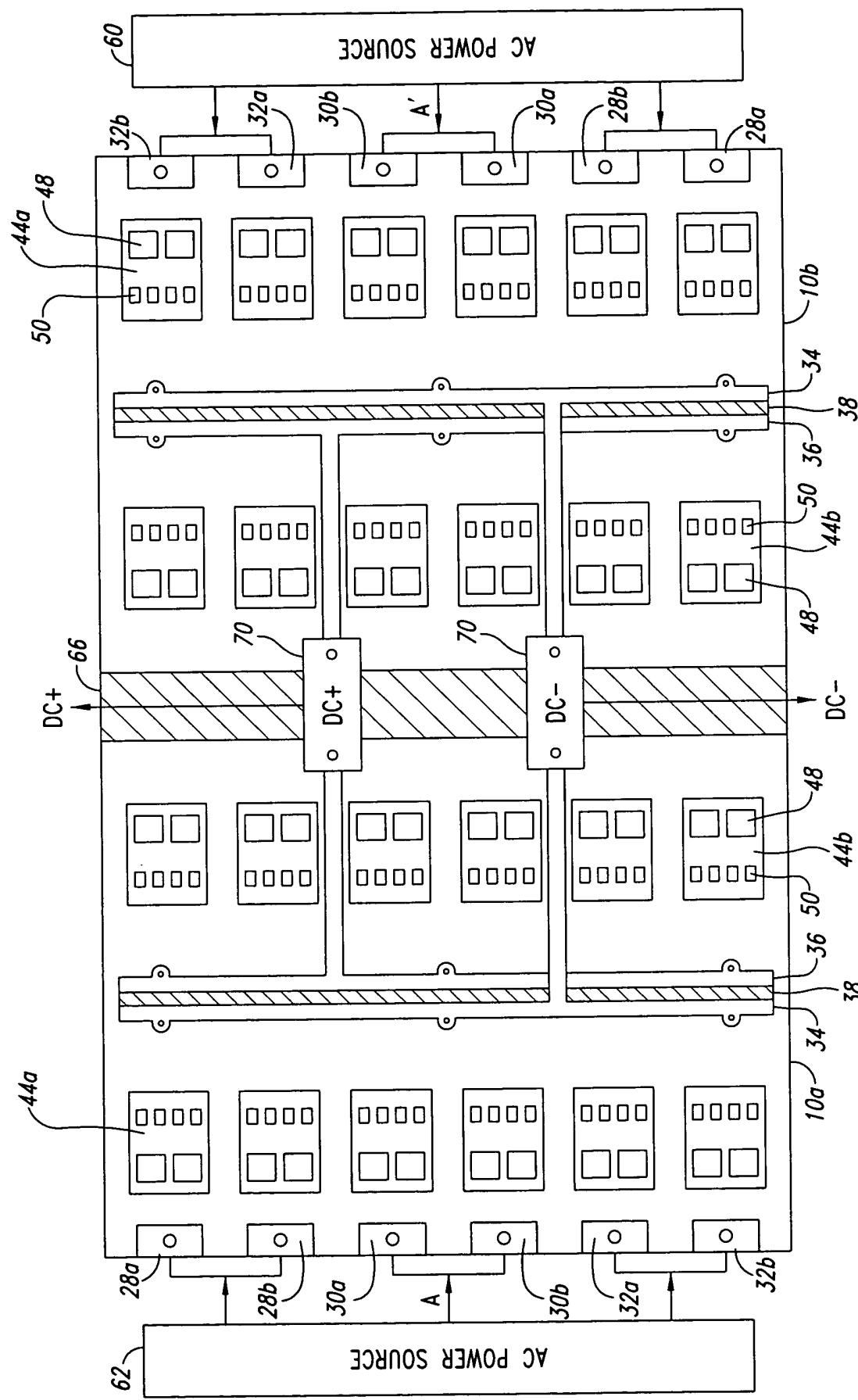


FIG. 13

Title: DUAL POWER MODULE POWER SYSTEM ARCHITECTURE

Inventor(s): Sayeed Ahmed et al. Express Mail No. EV336598819US Docket No. 130209.507

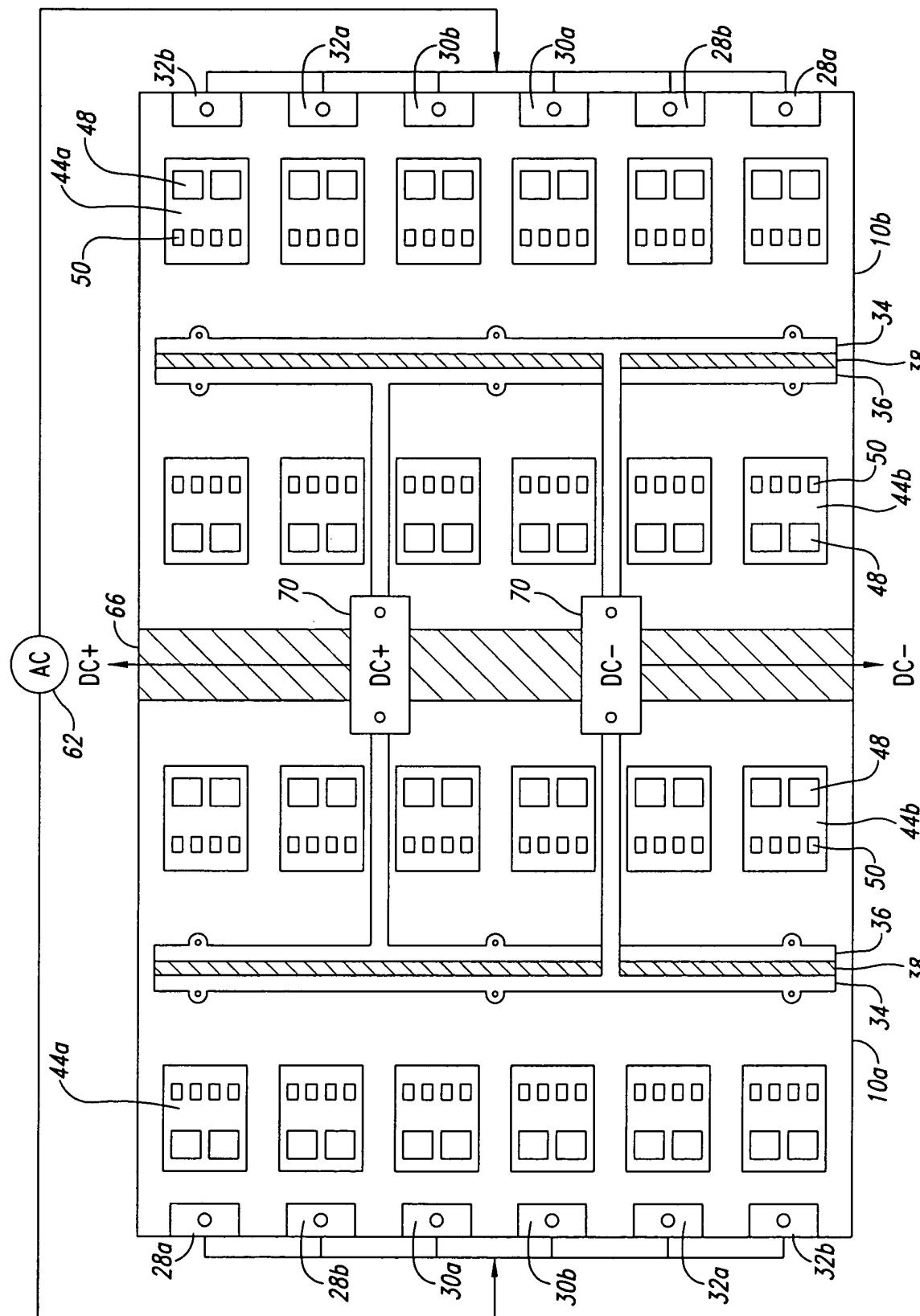


FIG. 14